

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/673,955	GONZALEZ ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Anand Patel	2116	

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,794,824	09-2004	Song et al.	315/169.4
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chung, Wai Man, The Usage of Dual Edge Triggered Flip-flops in Low Power, Low Voltage Applications, 01/03.
	V	Stan, Mircea R., Kevin Skadron, Power-Aware Computing, 12/03, IEEE Computer Society. 
	W	Mathew, Binu K., The Perception Processor, 08/04. 
	X	Hsu, Chung-Hsing, Wu-chun Feng, Jeremy S. Archuleta, Towards Efficient Supercomputing: A Quest for the Right Metric, 04/05, IEEE Workshop on High-Performance, Power-Aware Computing. 

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.